

ANNA UNIVERSITY, CHENNAI
AFFILIATED INSTITUTIONS
B.E. ELECTRONICS AND COMMUNICATION ENGINEERING
REGULATIONS – 2017
CHOICE BASED CREDIT SYSTEM
III SEMESTER CURRICULUM AND SYLLABI

SEMESTER III

SI. No	COURSE CODE	COURSE TITLE	CATEGORY	CONTACT PERIODS	L	T	P	C
THEORY								
1.	MA8352	Linear Algebra and Partial Differential Equations	BS	4	4	0	0	4
2.	EC8393	Fundamentals of Data Structures in C	ES	3	3	0	0	3
3.	EC8351	Electronic Circuits- I	PC	3	3	0	0	3
4.	EC8352	Signals and Systems	PC	4	4	0	0	4
5.	EC8392	Digital Electronics	PC	3	3	0	0	3
6.	EC8391	Control Systems Engineering	PC	3	3	0	0	3
PRACTICALS								
7.	EC8381	Fundamentals of Data Structures in C Laboratory	ES	4	0	0	4	2
8.	EC8361	Analog and Digital Circuits Laboratory	PC	4	0	0	4	2
9.	HS8381	Interpersonal Skills/Listening & Speaking	EEC	2	0	0	2	1
TOTAL				30	20	0	10	25

OBJECTIVES:

- To introduce the basic notions of groups, rings, fields which will then be used to solve related problems.
- To understand the concepts of vector space, linear transformations and diagonalization.
- To apply the concept of inner product spaces in orthogonalization.
- To understand the procedure to solve partial differential equations.
- To give an integrated approach to number theory and abstract algebra, and provide a firm basis for further reading and study in the subject.

UNIT I VECTOR SPACES 12

Vector spaces – Subspaces – Linear combinations and linear system of equations – Linear independence and linear dependence – Bases and dimensions.

UNIT II LINEAR TRANSFORMATION AND DIAGONALIZATION 12

Linear transformation - Null spaces and ranges - Dimension theorem - Matrix representation of a linear transformations - Eigenvalues and eigenvectors - Diagonalizability.

UNIT III INNER PRODUCT SPACES 12

Inner product, norms - Gram Schmidt orthogonalization process - Adjoint of linear operations - Least square approximation.

UNIT IV PARTIAL DIFFERENTIAL EQUATIONS 12

Formation – Solutions of first order equations – Standard types and equations reducible to standard types – Singular solutions – Lagrange’s linear equation – Integral surface passing through a given curve – Classification of partial differential equations - Solution of linear equations of higher order with constant coefficients – Linear non-homogeneous partial differential equations.

UNIT V FOURIER SERIES SOLUTIONS OF PARTIAL DIFFERENTIAL EQUATIONS 12

Dirichlet’s conditions – General Fourier series – Half range sine and cosine series - Method of separation of variables – Solutions of one dimensional wave equation and one-dimensional heat equation – Steady state solution of two-dimensional heat equation – Fourier series solutions in Cartesian coordinates.

TOTAL: 60 PERIODS

OUTCOMES:

Upon successful completion of the course, students should be able to:

- Explain the fundamental concepts of advanced algebra and their role in modern mathematics and applied contexts.
- Demonstrate accurate and efficient use of advanced algebraic techniques.
- Demonstrate their mastery by solving non - trivial problems related to the concepts and by proving simple theorems about the statements proven by the text.
- Able to solve various types of partial differential equations.
Able to solve engineering problems using Fourier series.

TEXTBOOKS:

1. Grewal B.S., “Higher Engineering Mathematics”, Khanna Publishers, New Delhi, 43rd Edition, 2014.
2. Friedberg, A.H., Insel, A.J. and Spence, L., “Linear Algebra”, Prentice Hall of India, New Delhi, 2004.

REFERENCES:

1. Burden, R.L. and Faires, J.D, "Numerical Analysis", 9th Edition, Cengage Learning, 2016.
2. James, G. "Advanced Modern Engineering Mathematics", Pearson Education, 2007.
3. Kolman, B. Hill, D.R., "Introductory Linear Algebra", Pearson Education, New Delhi, First Reprint, 2009.
4. Kumaresan, S., "Linear Algebra – A Geometric Approach", Prentice – Hall of India, New Delhi, Reprint, 2010.
5. Lay, D.C., "Linear Algebra and its Applications", 5th Edition, Pearson Education, 2015.
6. O'Neil, P.V., "Advanced Engineering Mathematics", Cengage Learning, 2007.
7. Strang, G., "Linear Algebra and its applications", Thomson (Brooks/Cole), New Delhi, 2005.
8. Sundarapandian, V. "Numerical Linear Algebra", Prentice Hall of India, New Delhi, 2008.

EC8393

FUNDAMENTALS OF DATA STRUCTURES IN C

L T P C
3 0 0 3

OBJECTIVES:

- To learn the features of C
- To learn the linear and non-linear data structures
- To explore the applications of linear and non-linear data structures
- To learn to represent data using graph data structure
- To learn the basic sorting and searching algorithms

UNIT I C PROGRAMMING BASICS

9

Structure of a C program – compilation and linking processes – Constants, Variables – Data Types – Expressions using operators in C – Managing Input and Output operations – Decision Making and Branching – Looping statements. Arrays – Initialization – Declaration – One dimensional and Two-dimensional arrays. Strings- String operations – String Arrays. Simple programs- sorting-searching – matrix operations.

UNIT II FUNCTIONS, POINTERS, STRUCTURES AND UNIONS

9

Functions – Pass by value – Pass by reference – Recursion – Pointers - Definition – Initialization – Pointers arithmetic. Structures and unions - definition – Structure within a structure - Union - Programs using structures and Unions – Storage classes, Pre-processor directives.

UNIT III LINEAR DATA STRUCTURES

9

Arrays and its representations – Stacks and Queues – Linked lists – Linked list-based implementation of Stacks and Queues – Evaluation of Expressions – Linked list based polynomial addition.

UNIT IV NON-LINEAR DATA STRUCTURES

9

Trees – Binary Trees – Binary tree representation and traversals – Binary Search Trees – Applications of trees. Set representations - Union-Find operations. Graph and its representations – Graph Traversals.

UNIT V SEARCHING AND SORTING ALGORITHMS

9

Linear Search – Binary Search. Bubble Sort, Insertion sort – Merge sort – Quick sort - Hash tables – Overflow handling.

TOTAL: 45 PERIODS

OUTCOMES:**Upon completion of the course, students will be able to:**

- Implement linear and non-linear data structure operations using C
- Suggest appropriate linear / non-linear data structure for any given data set.
- Apply hashing concepts for a given problem
- Modify or suggest new data structure for an application
- Appropriately choose the sorting algorithm for an application

TEXTBOOKS:

1. Pradip Dey and Manas Ghosh, —Programming in C, Second Edition, Oxford University Press, 2011.
2. Ellis Horowitz, Sartaj Sahni, Susan Anderson-Freed, —Fundamentals of Data Structures in C, Second Edition, University Press, 2008.

REFERENCES:

1. Mark Allen Weiss, —Data Structures and Algorithm Analysis in C, Second Edition, Pearson Education, 1996
2. Alfred V. Aho, John E. Hopcroft and Jeffrey D. Ullman, —Data Structures and Algorithms, Pearson Education, 1983.
3. Robert Kruse, C.L.Tondo, Bruce Leung, Shashi Mogalla , — Data Structures and Program Design in C, Second Edition, Pearson Education, 2007
4. Jean-Paul Tremblay and Paul G. Sorenson, —An Introduction to Data Structures with Applications, Second Edition, Tata McGraw-Hill, 1991.

EC8351**ELECTRONIC CIRCUITS I**

L	T	P	C
3	0	0	3

OBJECTIVES:

- To understand the methods of biasing transistors
- To design and analyze single stage and multistage amplifier circuits
- To analyze the frequency response of small signal amplifiers
- To design and analyze the regulated DC power supplies.
- To troubleshoot and fault analysis of power supplies.

UNIT I BIASING OF DISCRETE BJT, JFET AND MOSFET 9

BJT– Need for biasing - DC Load Line and Bias Point – DC analysis of Transistor circuits - Various biasing methods of BJT – Bias Circuit Design - Thermal stability - Stability factors - Bias compensation techniques using Diode, thermistor and sensistor – Biasing BJT Switching Circuits- JFET - DC Load Line and Bias Point - Various biasing methods of JFET - JFET Bias Circuit Design - MOSFET Biasing - Biasing FET Switching Circuits.

UNIT II BJT AMPLIFIERS 9

Small Signal Hybrid π equivalent circuit of BJT – Early effect - Analysis of CE, CC and CB amplifiers using Hybrid π equivalent circuits - AC Load Line Analysis- Darlington Amplifier - Bootstrap technique - Cascade, Cascode configurations - Differential amplifier, Basic BJT differential pair – Small signal analysis and CMRR.

UNIT III SINGLE STAGE FET, MOSFET AMPLIFIERS 9

Small Signal Hybrid π equivalent circuit of FET and MOSFET - Analysis of CS, CD and CG amplifiers using Hybrid π equivalent circuits - Basic FET differential pair- BiCMOS circuits.

UNIT IV FREQUENCY RESPONSE OF AMPLIFIERS

9

Amplifier frequency response – Frequency response of transistor amplifiers with circuit capacitors – BJT frequency response – short circuit current gain - cut off frequency – f_{α} , f_{β} and unity gain bandwidth – Miller effect - frequency response of FET - High frequency analysis of CE and MOSFET CS amplifier - Transistor Switching Times.

UNIT V POWER SUPPLIES AND ELECTRONIC DEVICE TESTING

9

Linear mode power supply - Rectifiers - Filters - Half-Wave Rectifier Power Supply - Full-Wave Rectifier Power Supply - Voltage regulators: Voltage regulation - Linear series, shunt and switching Voltage Regulators - Over voltage protection - BJT and MOSFET – Switched mode power supply (SMPS) - Power Supply Performance and Testing - Troubleshooting and Fault Analysis, Design of Regulated DC Power Supply.

TOTAL: 45 PERIODS

OUTCOMES:

After studying this course, the student should be able to:

- Acquire knowledge of
 - Working principles, characteristics and applications of BJT and FET
 - Frequency response characteristics of BJT and FET amplifiers
- Analyze the performance of small signal BJT and FET amplifiers - single stage and multi stage amplifiers
- Apply the knowledge gained in the design of Electronic circuits

TEXT BOOKS:

1. Donald. A. Neamen, Electronic Circuits Analysis and Design, 3rd Edition, Mc Graw Hill Education (India) Private Ltd., 2010. (Unit I-IV)
2. Robert L. Boylestad and Louis Nasheresky, “Electronic Devices and Circuit Theory”, 11th Edition, Pearson Education, 2013. (Unit V)

REFERENCES

1. Millman J, Halkias.C.and Sathyabrada Jit, Electronic Devices and Circuits, 4th Edition, Mc Graw Hill Education (India) Private Ltd., 2015.
2. Salivahanan and N. Suresh Kumar, Electronic Devices and Circuits, 4th Edition, , Mc Graw Hill Education (India) Private Ltd., 2017.
3. Floyd, Electronic Devices, Ninth Edition, Pearson Education, 2012.
4. David A. Bell, Electronic Devices & Circuits, 5th Edition, Oxford University Press, 2008.
5. Anwar A. Khan and Kanchan K. Dey, A First Course on Electronics, PHI, 2006.
6. Rashid M, Microelectronics Circuits, Thomson Learning, 2007.

OBJECTIVES:

- To understand the basic properties of signal & systems
- To know the methods of characterization of LTI systems in time domain
- To analyze continuous time signals and system in the Fourier and Laplace domain
- To analyze discrete time signals and system in the Fourier and Z transform domain

UNIT I CLASSIFICATION OF SIGNALS AND SYSTEMS 12

Standard signals- Step, Ramp, Pulse, Impulse, Real and complex exponentials and Sinusoids_ Classification of signals – Continuous time (CT) and Discrete Time (DT) signals, Periodic & Aperiodic signals, Deterministic & Random signals, Energy & Power signals - Classification of systems- CT systems and DT systems- – Linear & Nonlinear, Time-variant & Time-invariant, Causal & Non-causal, Stable & Unstable.

UNIT II ANALYSIS OF CONTINUOUS TIME SIGNALS 12

Fourier series for periodic signals - Fourier Transform – properties- Laplace Transforms and properties

UNIT III LINEAR TIME INVARIANT CONTINUOUS TIME SYSTEMS 12

Impulse response - convolution integrals- Differential Equation- Fourier and Laplace transforms in Analysis of CT systems - Systems connected in series / parallel.

UNIT IV ANALYSIS OF DISCRETE TIME SIGNALS 12

Baseband signal Sampling – Fourier Transform of discrete time signals (DTFT) – Properties of DTFT - Z Transform & Properties

UNIT V LINEAR TIME INVARIANT-DISCRETE TIME SYSTEMS 12

Impulse response – Difference equations-Convolution sum- Discrete Fourier Transform and Z Transform Analysis of Recursive & Non-Recursive systems-DT systems connected in series and parallel.

TOTAL: 60 PERIODS**OUTCOMES:****At the end of the course, the student should be able to:**

- To be able to determine if a given system is linear/causal/stable
- Capable of determining the frequency components present in a deterministic signal
- Capable of characterizing LTI systems in the time domain and frequency domain
- To be able to compute the output of an LTI system in the time and frequency domains

TEXT BOOK:

1. Allan V.Oppenheim, S.Wilsky and S.H.Nawab, "Signals and Systems", Pearson, 2015.(Unit 1-V)

REFERENCES

1. B. P. Lathi, "Principles of Linear Systems and Signals", Second Edition, Oxford, 2009.
2. R.E.Zeimer, W.H.Tranter and R.D.Fannin, "Signals & Systems - Continuous and Discrete", Pearson, 2007.
3. John Alan Stuller, "An Introduction to Signals and Systems", Thomson, 2007.

OBJECTIVES:

- To present the Digital fundamentals, Boolean algebra and its applications in digital systems
- To familiarize with the design of various combinational digital circuits using logic gates
- To introduce the analysis and design procedures for synchronous and asynchronous sequential circuits
- To explain the various semiconductor memories and related technology
- To introduce the electronic circuits involved in the making of logic gates

UNIT I DIGITAL FUNDAMENTALS 9

Number Systems – Decimal, Binary, Octal, Hexadecimal, 1's and 2's complements, Codes – Binary, BCD, Excess 3, Gray, Alphanumeric codes, Boolean theorems, Logic gates, Universal gates, Sum of products and product of sums, Minterms and Maxterms, Karnaugh map Minimization and Quine-McCluskey method of minimization.

UNIT II COMBINATIONAL CIRCUIT DESIGN 9

Design of Half and Full Adders, Half and Full Subtractors, Binary Parallel Adder – Carry look ahead Adder, BCD Adder, Multiplexer, Demultiplexer, Magnitude Comparator, Decoder, Encoder, Priority Encoder.

UNIT III SYNCHRONOUS SEQUENTIAL CIRCUITS 9

Flip flops – SR, JK, T, D, Master/Slave FF – operation and excitation tables, Triggering of FF, Analysis and design of clocked sequential circuits – Design - Moore/Mealy models, state minimization, state assignment, circuit implementation – Design of Counters- Ripple Counters, Ring Counters, Shift registers, Universal Shift Register.

UNIT IV ASYNCHRONOUS SEQUENTIAL CIRCUITS 9

Stable and Unstable states, output specifications, cycles and races, state reduction, race free assignments, Hazards, Essential Hazards, Pulse mode sequential circuits, Design of Hazard free circuits.

UNIT V MEMORY DEVICES AND DIGITAL INTEGRATED CIRCUITS 9

Basic memory structure – ROM -PROM – EPROM – EEPROM –EAPROM, RAM – Static and dynamic RAM - Programmable Logic Devices – Programmable Logic Array (PLA) - Programmable Array Logic (PAL) – Field Programmable Gate Arrays (FPGA) - Implementation of combinational logic circuits using PLA, PAL.

Digital integrated circuits: Logic levels, propagation delay, power dissipation, fan-out and fan-in, noise margin, logic families and their characteristics-RTL, TTL, ECL, CMOS

TOTAL: 45 PERIODS**OUTCOMES:****At the end of the course:**

- Use digital electronics in the present contemporary world
- Design various combinational digital circuits using logic gates
- Do the analysis and design procedures for synchronous and asynchronous sequential circuits
- Use the semiconductor memories and related technology
- Use electronic circuits involved in the design of logic gates

TEXT BOOK:

1. M. Morris Mano and Michael D. Ciletti, "Digital Design", 5th Edition, Pearson, 2014.

REFERENCES:

1. Charles H.Roth. "Fundamentals of Logic Design", 6th Edition, Thomson Learning, 2013.
2. Thomas L. Floyd, "Digital Fundamentals", 10th Edition, Pearson Education Inc, 2011
3. S.Salivahanan and S.Arivazhagan"Digital Electronics", 1st Edition, Vikas Publishing House pvt Ltd, 2012.
4. Anil K.Maini "Digital Electronics", Wiley, 2014.
5. A.Anand Kumar "Fundamentals of Digital Circuits", 4th Edition, PHI Learning Private Limited, 2016.
6. Soumitra Kumar Mandal " Digital Electronics", McGraw Hill Education Private Limited, 2016.

EC8391	CONTROL SYSTEMS ENGINEERING	L	T	P	C
		3	0	0	3

OBJECTIVES:

- To introduce the components and their representation of control systems
- To learn various methods for analyzing the time response, frequency response and stability of the systems.
- To learn the various approach for the state variable analysis.

UNIT I SYSTEMS COMPONENTS AND THEIR REPRESENTATION 9

Control System: Terminology and Basic Structure-Feed forward and Feedback control theory-Electrical and Mechanical Transfer Function Models-Block diagram Models-Signal flow graphs models-DC and AC servo Systems-Synchronous -Multivariable control system

UNIT II TIME RESPONSE ANALYSIS 9

Transient response-steady state response-Measures of performance of the standard first order and second order system-effect on an additional zero and an additional pole-steady error constant and system- type number-PID control-Analytical design for PD, PI,PID control systems

UNIT III FREQUENCY RESPONSE AND SYSTEM ANALYSIS 9

Closed loop frequency response-Performance specification in frequency domain-Frequency response of standard second order system- Bode Plot - Polar Plot- Nyquist plots-Design of compensators using Bode plots-Cascade lead compensation-Cascade lag compensation-Cascade lag-lead compensation

UNIT IV CONCEPTS OF STABILITY ANALYSIS 9

Concept of stability-Bounded - Input Bounded - Output stability-Routh stability criterion-Relative stability-Root locus concept-Guidelines for sketching root locus-Nyquist stability criterion.

UNIT V CONTROL SYSTEM ANALYSIS USING STATE VARIABLE METHODS 9

State variable representation-Conversion of state variable models to transfer functions-Conversion of transfer functions to state variable models-Solution of state equations-Concepts of Controllability and Observability-Stability of linear systems-Equivalence between transfer function and state variable representations-State variable analysis of digital control system-Digital control design using state feedback.

TOTAL:45 PERIODS

OUTCOMES:

Upon completion of the course, the student should be able to:

- Identify the various control system components and their representations.
- Analyze the various time domain parameters.
- Analysis the various frequency response plots and its system.
- Apply the concepts of various system stability criteria.
- Design various transfer functions of digital control system using state variable models.

TEXT BOOK:

1. M.Gopal, "Control System – Principles and Design", Tata McGraw Hill, 4th Edition, 2012.

REFERENCES:

1. J.Nagrath and M.Gopal, "Control System Engineering", New Age International Publishers, 5th Edition, 2007.
2. K. Ogata, 'Modern Control Engineering', 5th edition, PHI, 2012.
3. S.K.Bhattacharya, Control System Engineering, 3rd Edition, Pearson, 2013.
4. Benjamin.C.Kuo, "Automatic control systems", Prentice Hall of India, 7th Edition, 1995.

EC8381**FUNDAMENTALS OF DATA STRUCTURES IN C LABORATORY****L T P C
0 0 4 2****OBJECTIVES:**

- To understand and implement basic data structures using C
- To apply linear and non-linear data structures in problem solving.
- To learn to implement functions and recursive functions by means of data structures
- To implement searching and sorting algorithms

LIST OF EXERCISES

1. Basic C Programs – looping, data manipulations, arrays
2. Programs using strings – string function implementation
3. Programs using structures and pointers
4. Programs involving dynamic memory allocations
5. Array implementation of stacks and queues
6. Linked list implementation of stacks and queues
7. Application of Stacks and Queues
8. Implementation of Trees, Tree Traversals
9. Implementation of Binary Search trees
10. Implementation of Linear search and binary search
11. Implementation Insertion sort, Bubble sort, Quick sort and Merge Sort
12. Implementation Hash functions, collision resolution technique

TOTAL:60 PERIODS**OUTCOMES:**

Upon completion of the course, the students will be able to:

- Write basic and advanced programs in C
- Implement functions and recursive functions in C
- Implement data structures using C
- Choose appropriate sorting algorithm for an application and implement it in a modularized way

OBJECTIVES:

The student should be made to:

- Study the Frequency response of CE, CB and CC Amplifier
- Learn the frequency response of CS Amplifiers
- Study the Transfer characteristics of differential amplifier
- Perform experiment to obtain the bandwidth of single stage and multistage amplifiers
- Perform SPICE simulation of Electronic Circuits
- Design and implement the Combinational and sequential logic circuits

LIST OF ANALOG EXPERIMENTS:

1. Design of Regulated Power supplies
2. Frequency Response of CE, CB, CC and CS amplifiers
3. Darlington Amplifier
4. Differential Amplifiers - Transfer characteristics, CMRR Measurement
5. Cascode and Cascade amplifiers
6. Determination of bandwidth of single stage and multistage amplifiers
7. Analysis of BJT with Fixed bias and Voltage divider bias using Spice
8. Analysis of FET, MOSFET with fixed bias, self-bias and voltage divider bias using simulation software like Spice
9. Analysis of Cascode and Cascade amplifiers using Spice
10. Analysis of Frequency Response of BJT and FET using Spice

LIST OF DIGITAL EXPERIMENTS

1. Design and implementation of code converters using logic gates(i) BCD to excess-3 code and vice versa (ii) Binary to gray and vice-versa
2. Design and implementation of 4 bit binary Adder/ Subtractor and BCD adder using IC 7483
3. Design and implementation of Multiplexer and De-multiplexer using logic gates
4. Design and implementation of encoder and decoder using logic gates
5. Construction and verification of 4 bit ripple counter and Mod-10 / Mod-12 Ripple counters
6. Design and implementation of 3-bit synchronous up/down counter

TOTAL : 60 PERIODS

OUTCOMES:

On completion of this laboratory course, the student should be able to:

- Design and Test rectifiers, filters and regulated power supplies.
- Design and Test BJT/JFET amplifiers.
- Differentiate cascode and cascade amplifiers.
- Analyze the limitation in bandwidth of single stage and multi stage amplifier
- Measure CMRR in differential amplifier
- Simulate and analyze amplifier circuits using PSpice.
- Design and Test the digital logic circuits.

LAB REQUIREMENTS FOR A BATCH OF 30 STUDENTS, 2 STUDENTS / EXPERIMENT:

S.NO	EQUIPMENTS FOR ANALOG LAB
1	CRO/DSO (30MHz) – 15 Nos.
2	Signal Generator /Function Generators (3 MHz) – 15 Nos
3	Dual Regulated Power Supplies (0 – 30V) – 15 Nos.
4	Standalone desktop PCs with SPICE software – 15 Nos.
5	Transistor/FET (BJT-NPN-PNP and NMOS/PMOS) – 50 Nos
6	Components and Accessories: Resistors, Capacitors, Inductors, diodes, Zener Diodes, Bread Boards, Transformers.
7	SPICE Circuit Simulation Software: (any public domain or commercial software)

S.NO	EQUIPMENTS FOR DIGITAL LAB
1	Dual power supply/ single mode power supply - 15 Nos
2	IC Trainer Kit - 15 Nos
3	Bread Boards - 15 Nos
4	Seven segment display -15 Nos
5	Multimeter - 15 Nos
6	ICs each 50 Nos 7400/ 7402 / 7404 / 7486 / 7408 / 7432 / 7483 / 74150 / 74151 / 74147 / 7445 / 7476/7491/ 555 / 7494 / 7447 / 74180 / 7485 / 7473 / 74138 / 7411 / 7474

HS8381	INTERPERSONAL SKILLS/LISTENING&SPEAKING	L	T	P	C
		0	0	2	1

OBJECTIVES:

The Course will enable learners to:

- Equip students with the English language skills required for the successful undertaking of academic studies with primary emphasis on academic speaking and listening skills.
- Provide guidance and practice in basic general and classroom conversation and to engage in specific academic speaking activities.
- improve general and academic listening skills
- Make effective presentations.

UNIT I

Listening as a key skill- its importance- speaking - give personal information - ask for personal information - express ability - enquire about ability - ask for clarification Improving pronunciation - pronunciation basics taking lecture notes - preparing to listen to a lecture - articulate a complete idea as opposed to producing fragmented utterances.

UNIT II

Listen to a process information- give information, as part of a simple explanation - conversation starters: small talk - stressing syllables and speaking clearly - intonation patterns - compare and contrast information and ideas from multiple sources- converse with reasonable accuracy over a wide range of everyday topics.

UNIT III

Lexical chunking for accuracy and fluency- factors influence fluency, deliver a five-minute informal talk - greet - respond to greetings - describe health and symptoms - invite and offer - accept - decline - take leave - listen for and follow the gist- listen for detail

UNIT IV

Being an active listener: giving verbal and non-verbal feedback - participating in a group discussion - summarizing academic readings and lectures conversational speech listening to and participating in conversations - persuade.

UNIT V

Formal and informal talk - listen to follow and respond to explanations, directions and instructions in academic and business contexts - strategies for presentations and interactive communication - group/pair presentations - negotiate disagreement in group work.

TOTAL : 30 PERIODS

OUTCOMES:

At the end of the course Learners will be able to:

- Listen and respond appropriately.
- Participate in group discussions
- Make effective presentations
- Participate confidently and appropriately in conversations both formal and informal

TEXT BOOKS:

1. Brooks, Margret. Skills for Success. Listening and Speaking. Level 4 Oxford University Press, Oxford: 2011.
2. Richards, C. Jack. & David Bholke. Speak Now Level 3. Oxford University Press, Oxford: 2010

REFERENCES

1. Bhatnagar, Nitin and Mamta Bhatnagar. Communicative English for Engineers and Professionals. Pearson: New Delhi, 2010.
2. Hughes, Glyn and Josephine Moate. Practical English Classroom. Oxford University Press: Oxford, 2014.
3. Vargo, Mari. Speak Now Level 4. Oxford University Press: Oxford, 2013.
4. Richards C. Jack. Person to Person (Starter). Oxford University Press: Oxford, 2006.
5. Ladousse, Gillian Porter. Role Play. Oxford University Press: Oxford, 2014